REMARKS

The amendment to the claims is simply to correct a typographical error and should be entered.

In response to arguments, two points are made. First, it is pointed out that Velazquez teaches an array. This array is believed to be the converter array 140 since column 8, lines 7-33 are cited under paragraph 3 on page 2 of the office action. However, the array 140 includes M analog-to-digital converters. See column 7, lines 8-11. Thus, the interpretation the Examiner adopts from the Electronic Computer Glossary is clearly inappropriate. Resort to the glossary is improper where the cited reference itself defines what it is he is talking about. What he is talking about is an array of analog-to-digital converters, not a storage. Therefore, for this first reason, reconsideration is requested.

Secondly, it is noted that there is no adjusting of position or data in the alleged storage device because the compensation includes rate changers to adjust the signal rate from the rate used by the converters in the array to the effective sample rate of the full system. Cited in support thereof, again, is column 8, lines 7-33. It is explained in that material that if the individual converters in the array are sampling at 1/M, the effective sample rate of the full system, then digital up samplers can be used to increase the rate by a factor of M. But this is akin to the situation where a series of roller derby rolling vehicles are in a race in a line one after the other. When they come to a hill, they are accelerated, but they are all accelerated by the same hill and, thus, their positions never change, they just simply increase in speed. There is no suggestion in this material that the position of any data element is changed. Doing so would be unusual and no provision is made for how to reassemble the data once positions are changed. Since there is no discussion of changing the positions of the data, it is respectfully submitted that reconsideration would be appropriate.
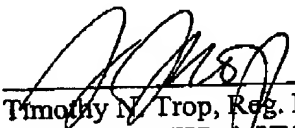
With respect to the rejection of claim 10, it is reiterated from the prior response that nothing that corresponds to the sampling block or the detector block is cited. Certainly, the rejection is not commensurate with the claims and, therefore, reconsideration is requested. There is no identification of a sampling block or detector block to detect when the frequency of the sampling clock is different from the rate of the incoming data. Nor, as described above, is there any storage device that adjusts the position of the data in response to detecting the difference in

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frequency of the sampling clock in the incoming data. This is due, at least initially, because there is no detecting of that difference in frequency and, finally, because there is no storage device that adjusts position.

Therefore, reconsideration is requested.

Respectfully submitted,

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